

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	114	generat\$3 same first clock signal\$1 same error signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:43
L2	92	generat\$3 same second clock signal\$1 same error signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:43
L3	13	(phase different or phase comparator or phase detector) same (I1 same I2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:44
L4	67591	generat\$3 same error signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:45
L5	13	I3 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 19:18
L6	1142	clock distribution circuit or clock distributor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:45
L7	1	I5 and I6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:45
L8	17550	714/799 or 714/699 or 714/700 or 714/707 or 714/798 or 714/814 or 713/400 or 713/503 or 713/500 or 714/?	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:46

EAST Search History

L9	2	I5 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:58
----	---	-----------	---	-----	----	------------------

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	114	generat\$3 same first clock signal\$1 same error signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:43
L2	92	generat\$3 same second clock signal\$1 same error signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:43
L3	13	(phase different or phase comparator or phase detector) same (I1 same I2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:44
L4	67591	generat\$3 same error signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:45
L5	13	I3 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 19:45
L6	1142	clock distribution circuit or clock distributor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:45
L7	1	I5 and I6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:45
L8	17550	714/799 or 714/699 or 714/700 or 714/707 or 714/798 or 714/814 or 713/400 or 713/503 or 713/500 or 714/?	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:46

EAST Search History

L9	2	I5 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 18:58
L14	1	(error signal AND first clock signal AND second clock signal AND phase different AND clock signal).clm.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/01/19 20:34
L15	4609	generat\$3 same (error signal\$1 or phase error signal\$1) same (phase comparator or phase detector)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 20:35
L16	182	first clock circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 20:35
L17	239	second clock circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 20:36
L18	32	generat\$3 same first clock signal same I16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 20:36
L19	20	generat\$3 same second clock signal same I17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 20:36
L20	7	compar\$3 and (I18 same I19)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 20:36

EAST Search History

L22	1	I15 and (I18 and I19)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 20:37
L23	101	generat\$3 same (recovered clock signal\$1 or recovered first clock signal or recovered second clock signal\$1) same (phase error or error signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 20:39
L25	33	I15 and I23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/19 20:40